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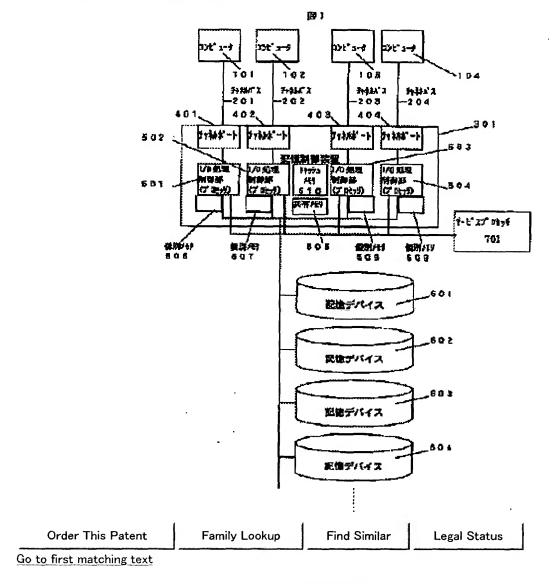
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Search scope: US Granted US Applications EP-A EP-B WO JP (bibliographic data only) DE-C DE-A DE-T DE-U

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Years: 1981-2003

Patent/Publication No.: JP2002108567



JP2002108567 A STORAGE CONTROL APPARATUS HITACHI LTD HITACHI SOFTWARE ENG CO LTD

Inventor(s):IDO KENJI ;GOTO YOICHI ;YOKOHATA SHIZUO ;HONMA SHIGEO ;YOSHINO TOSHIYUKI Application No. 2000300563 JP2000300563 JP, Filed 20000928,A1 Published 20020412Published 20020412

Abstract: PROBLEM TO BE SOLVED: To process an I/O to be preferentially processed without being affected by any other I/O processing while maintaining performance by dividing the I/O processing into the priority processing and the non-priority processing, and processing the non-priority I/O while suppressing the processing.

SOLUTION: A storage control apparatus 301 is provided with a memory 505 common to I/O processing control parts 501-504, and manages information for dividing plural I/O processing into the priority processing and the non-

priority processing, and for controlling them in the memory 505. Thus, it is possible to operate the non-priority I/O while suppressing its processing.

Int'l Class: G06F00306; G06F01208 G06F015177

Patents Citing this One: No US, EP, or WO patents/search reports have cited this patent. MicroPatent Reference

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